FIG. 1D ELECTRIC CURRENT TIME STROKE ~ 1CYCLE LOW ELECTRIC CURRENT STATE OFF 7 NO FIG. 1A PRIOR ARI FIG. 1C

PRIOR ARI

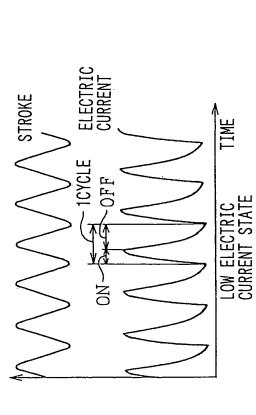
ON STROKE

CURRENT

HIGH ELECTRIC

TIME

CURRENT STATE



ELECTRIC CURRENT

NO

TIME

HIGH ELECTRIC CURRENT STATE

-0FF

STROKE

1CYCLE

FIG. 2

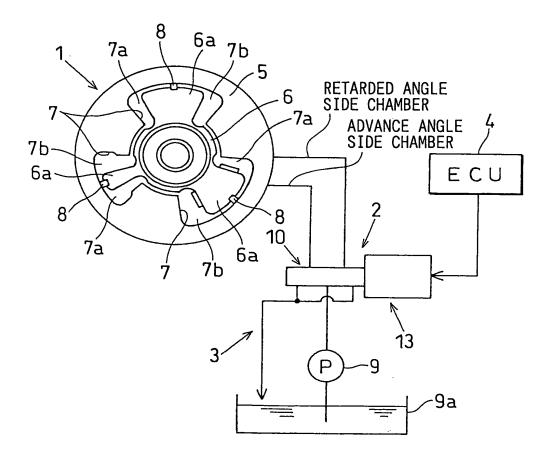


FIG. 3

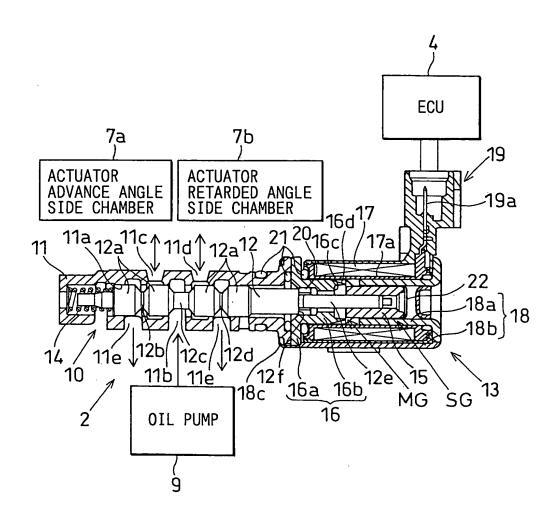


FIG. 4

23

CPU

DRIVER

OCV

A/DC

25

FIG. 5A PRIOR ART

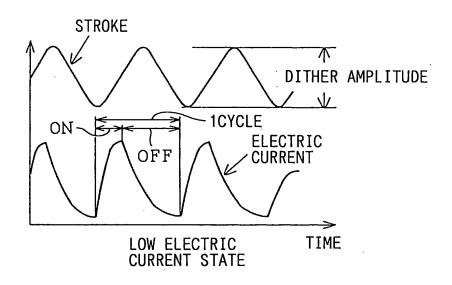


FIG. 5B PRIOR ART

